

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hidetoshi Nishikawa                          Art Unit : 2819  
Serial No. : 10/561,552                                  Examiner : Vibol Tan  
Filed : March 20, 2006                                  Conf. No. : 9795  
Title : SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

**Mail Stop Amendment**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF APRIL 30, 2007

Please amend the above-identified application as follows: